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Docket No. 0756-2061

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Patent Application of

Yusuhiko TAKEMURA

Serial No. 09/453,586 ✓

Filed: December 3, 1999

For: ELECTRO-OPTICAL DEVICE AND)

METHOD OF DRIVING AND)

MANUFACTURING THE SAME)

Art Unit: 2673

Examiner: J. Nguyen

Technology Center 2600

CERTIFICATE OF MAILING

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Washington, D.C. 20231, on 8-8-02

AMENDMENT

Honorable Commissioner of Patents

Washington, D.C. 20231

Sir:

In response to the Office Action dated April 8, 2002 please amend the above-
identified application as follows:

IN THE CLAIMS:

Please amend claims 2, 8, 52 and 55-66 as follows:

2. (Twice Amended) An operation method of a semiconductor device
comprising:

a substrate having an insulating surface;

a first signal line extending over said substrate;

a first bottom gate type thin film transistor having a channel region comprising
crystalline silicon formed over said substrate wherein a gate of said first thin film
transistor is connected to said first signal line;

a second signal line extending across said first signal line;

a second bottom gate type thin film transistor having a channel region comprising
crystalline silicon formed over said substrate wherein a gate of said second thin film